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STUDY MODULE DESCRIPTION FORM							
				Code 1010325341010324814			
Field of			Profile of study (general academic, practical) (brak)	Year /Semester			
	path/specialty	19	Subject offered in:	Course (compulsory, elective)			
		nd Computer Systems in	Polish	obligatory			
Cycle of	•		Form of study (full-time,part-time)				
	Second-c	ycle studies	part-	time			
No. of h				No. of credits			
Lectur	- Olacoo	· · · · · · · · · · · · · · · · · · ·	Project/seminars:	- 1			
Status o	of the course in the study	program (Basic, major, other) (brak)	(university-wide, from another fi	eld) (brak)			
Education	on areas and fields of sci	· /		ECTS distribution (number			
				and %)			
techr	nical sciences			1 100%			
Resp	onsible for subj	ect / lecturer:					
Dr inż. Jerzy Frąckowiak email: jerzy.frackowiak@put.poznan.pl tel. 616652382 Elektryczny ul. Piotrowo 3A, 60-965 Poznań							
Prere	quisites in term	s of knowledge, skills and	d social competencies:				
1	Knowledge	Basic knowledge of automation, control theory, PLCs and microcontrollers.					
2	Skills	The ability to understand and interpret the messages conveyed and effective self.					
3	Social competencies	Awareness of the need to broaden their competence.					
Assu	mptions and obj	ectives of the course:					
Knowle	edge of PLC cooperat	ion with microcontrollers.					
	Study outco	mes and reference to the	educational results for	a field of study			
Know	vledge:						
1. knowledge of PLC cooperation with microcontrollers - [K_W08++]							
2. selected interrupt PLC and microcontroller - [K_W08++]							
Skills		go to work DI Co and:	loro [V 1145 · · 1				
use the acquired knowledge to work PLCs and microcontrollers - [K_U15++] capacity for independent thinking and creative action - [K_U15++]							
Social competencies:							
Assessment methods of study outcomes							
Lecture:							
- final t	est.						
Course description							

PLCs - serial port, free port transmission mode, the selected interrupt PLC and microcontroller.

Faculty of Electrical Engineering

Basic bibliography:

- 1. Kamiński K.: "Programowanie w Step 7 Microwin", GRYF, Warszawa 2006
- 2. Dokumentacja sterownika S7-1200 firmy Siemens.

Additional bibliography:

1. Bubnicki Z.: "Teoria i algorytmy sterowania", Wydawnictwo Naukowe PWN, Warszawa 2002.

Result of average student's workload

Activity	Time (working hours)
1. participation in lectures	8
2. consultations for lectures	6
3. preparation for the completion of lectures	10
4. credit lecture	1

Student's workload

Source of workload	hours	ECTS
Total workload	25	1
Contact hours	15	1
Practical activities	0	0